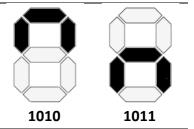
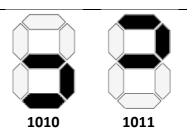
Group 1 - Team 1

Design combinational logic circuit to control a 7-segment display. The input is a 4-bit number in BCD format. On the outputs have to appear seven signals to control each display's diodes. Furthermore, for the bit combination 1010 and 1011 on display have to appear special character. The other combinations of bits (1100, 1101, 1110, 1111) don't belong to the domain of the function. **Design the system in the form of a schematic with logic gates.**



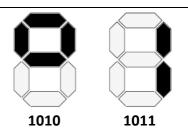
Group 1 - Team 2

Design combinational logic circuit to control a 7-segment display. The input is a 4-bit number in BCD format. On the outputs have to appear seven signals to control each display's diodes. Furthermore, for the bit combination 1010 and 1011 on display have to appear special character. The other combinations of bits (1100, 1101, 1110, 1111) don't belong to the domain of the function. **Design the system in the form of a schematic with logic gates.**

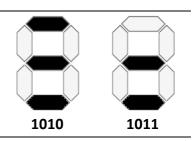


Group 1 - Team 3

Design combinational logic circuit to control a 7-segment display. The input is a 4-bit number in BCD format. On the outputs have to appear seven signals to control each display's diodes. Furthermore, for the bit combination 1010 and 1011 on display have to appear special character. The other combinations of bits (1100, 1101, 1110, 1111) don't belong to the domain of the function. **Design the system in the form of a schematic with logic gates.**

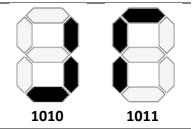


Group 1 - Team 4



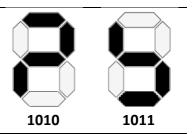
Group 1 - Team 5

Design combinational logic circuit to control a 7-segment display. The input is a 4-bit number in BCD format. On the outputs have to appear seven signals to control each display's diodes. Furthermore, for the bit combination 1010 and 1011 on display have to appear special character. The other combinations of bits (1100, 1101, 1110, 1111) don't belong to the domain of the function. **Design the system in the form of a schematic with logic gates.**



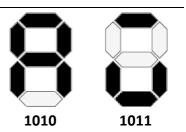
Group 1 - Team 6

Design combinational logic circuit to control a 7-segment display. The input is a 4-bit number in BCD format. On the outputs have to appear seven signals to control each display's diodes. Furthermore, for the bit combination 1010 and 1011 on display have to appear special character. The other combinations of bits (1100, 1101, 1110, 1111) don't belong to the domain of the function. **Design the system in the form of a schematic with logic gates.**

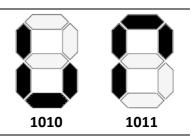


Group 1 - Team 7

Design combinational logic circuit to control a 7-segment display. The input is a 4-bit number in BCD format. On the outputs have to appear seven signals to control each display's diodes. Furthermore, for the bit combination 1010 and 1011 on display have to appear special character. The other combinations of bits (1100, 1101, 1110, 1111) don't belong to the domain of the function. **Design the system in the form of a schematic with logic gates.**

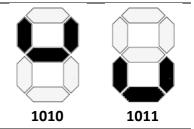


Group 1 - Team 8



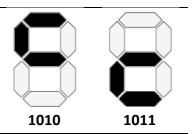
Group 2 - Team 1

Design combinational logic circuit to control a 7-segment display. The input is a 4-bit number in BCD format. On the outputs have to appear seven signals to control each display's diodes. Furthermore, for the bit combination 1010 and 1011 on display have to appear special character. The other combinations of bits (1100, 1101, 1110, 1111) don't belong to the domain of the function. **Design the system in the form of a schematic with logic gates.**



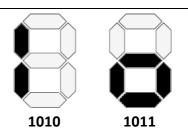
Group 2 - Team 2

Design combinational logic circuit to control a 7-segment display. The input is a 4-bit number in BCD format. On the outputs have to appear seven signals to control each display's diodes. Furthermore, for the bit combination 1010 and 1011 on display have to appear special character. The other combinations of bits (1100, 1101, 1110, 1111) don't belong to the domain of the function. **Design the system in the form of a schematic with logic gates.**

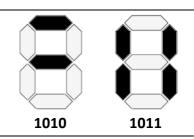


Group 2 - Team 3

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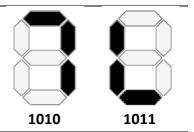


Group 2 - Team 4



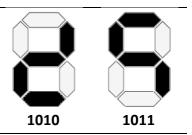
Group 2 - Team 5

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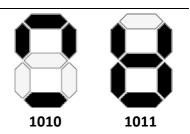
Group 2 - Team 6

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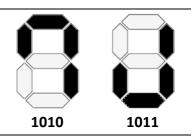


Group 2 - Team 7

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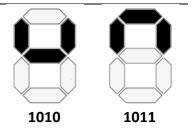


Group 2 - Team 8



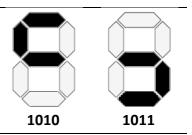
Group 3 - Team 1

Design combinational logic circuit to control a 7-segment display. The input is a 4-bit number in BCD format. On the outputs have to appear seven signals to control each display's diodes. Furthermore, for the bit combination 1010 and 1011 on display have to appear special character. The other combinations of bits (1100, 1101, 1110, 1111) don't belong to the domain of the function. **Design the system in the form of a schematic with logic gates.**



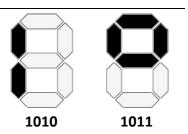
Group 3 - Team 2

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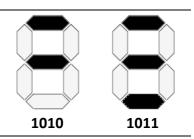


Group 3 - Team 3

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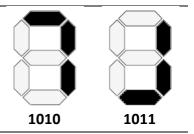


Group 3 - Team 4



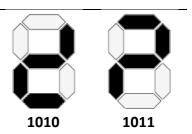
Group 3 - Team 5

Design combinational logic circuit to control a 7-segment display. The input is a 4-bit number in BCD format. On the outputs have to appear seven signals to control each display's diodes. Furthermore, for the bit combination 1010 and 1011 on display have to appear special character. The other combinations of bits (1100, 1101, 1110, 1111) don't belong to the domain of the function. **Design the system in the form of a schematic with logic gates.**



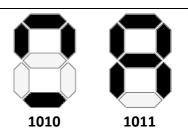
Group 3 - Team 6

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Group 3 - Team 7

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Group 3 - Team 8

